


RESILEX WEBINAR : 12th November 2025

The CEA logo is a red square with the lowercase letters 'cea' in white, with a horizontal line underneath the 'a'.

cea

An aerial photograph showing a large industrial facility with several large buildings, some with solar panels on their roofs. The facility is surrounded by green fields and a road. In the background, there are mountains and a body of water under a blue sky with clouds.

From silicon waste to high-purity solar cells

Mickael ALBARIC

CEA, INES

The ReSiLex logo features the word 'ReSiLex' in a dark blue font. The 'e' is replaced by a stylized sun icon with rays, and the 'i' is replaced by a stylized solar cell icon with a grid pattern. A green circular arc is positioned behind the 'Si' and 'lex' parts of the text.

ReSiLex

The INES logo consists of a stylized sun icon with rays on the left and the text 'ines' in a bold, lowercase font on the right. Below 'ines' is the full name 'INSTITUT NATIONAL DE L'ENERGIE SOLAIRE' in a smaller, uppercase font.

ines
INSTITUT NATIONAL
DE L'ENERGIE SOLAIRE

CEA, scientific and technology research key player

CEA is a key research player, at the service of the French state, economy and citizen.

Based on an excellent fundamental research, CEA brings solutions in four main domains:



Low Carbon Energy
(nuclear and renewable)



Digital



Future Healthcare



Defence and Security

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**Low Carbon
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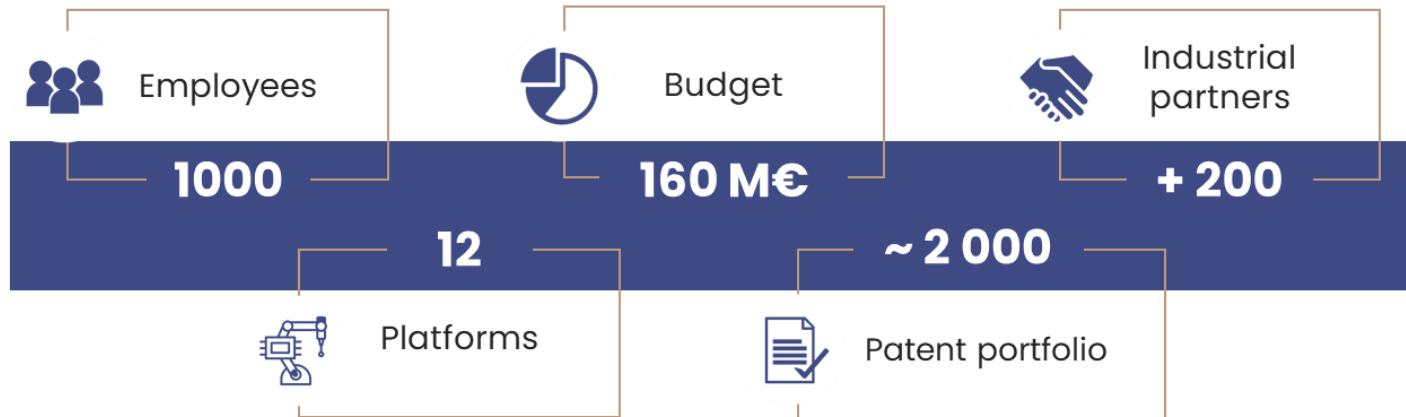
liten

**Research Institute for
Energy Transition**

Our mission since 20 years

Developing **cutting-edge technologies** to reach **carbon neutrality by 2050**, **empowering industries** and **catalyzing value** and **job creation** across France and Europe

Liten, a leader in the research for the energy transition



Grenoble GIANT Campus



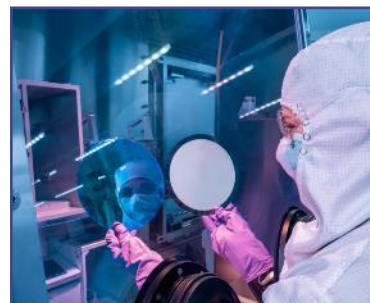
Chambéry INES Campus



Strategic Research Areas



Photovoltaics



Batteries



Hydrogen, e-fuel & e-molecules



Systems, grids & energy efficiency



Materials & Circular economy

Zoom on the Solar Platform

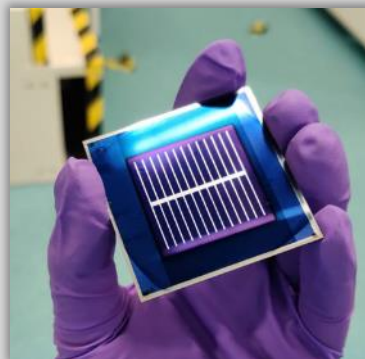
High efficiency PV technologies developed by CEA at INES



Heterojunction (SHJ) & TOPCon

25% efficiency
▶ to industry

- Industrialisation towards Gigafactories
- Sustainability : Indium, Silver...



TANDEM Si/Perovskite

29.8% on 9 cm²
▶ 30% on large
format

- Efficiency
- Stability & Processes



Advanced and eco-designed moduling

- Shingling, paving
- Interconnection
- Ecodesign : new material

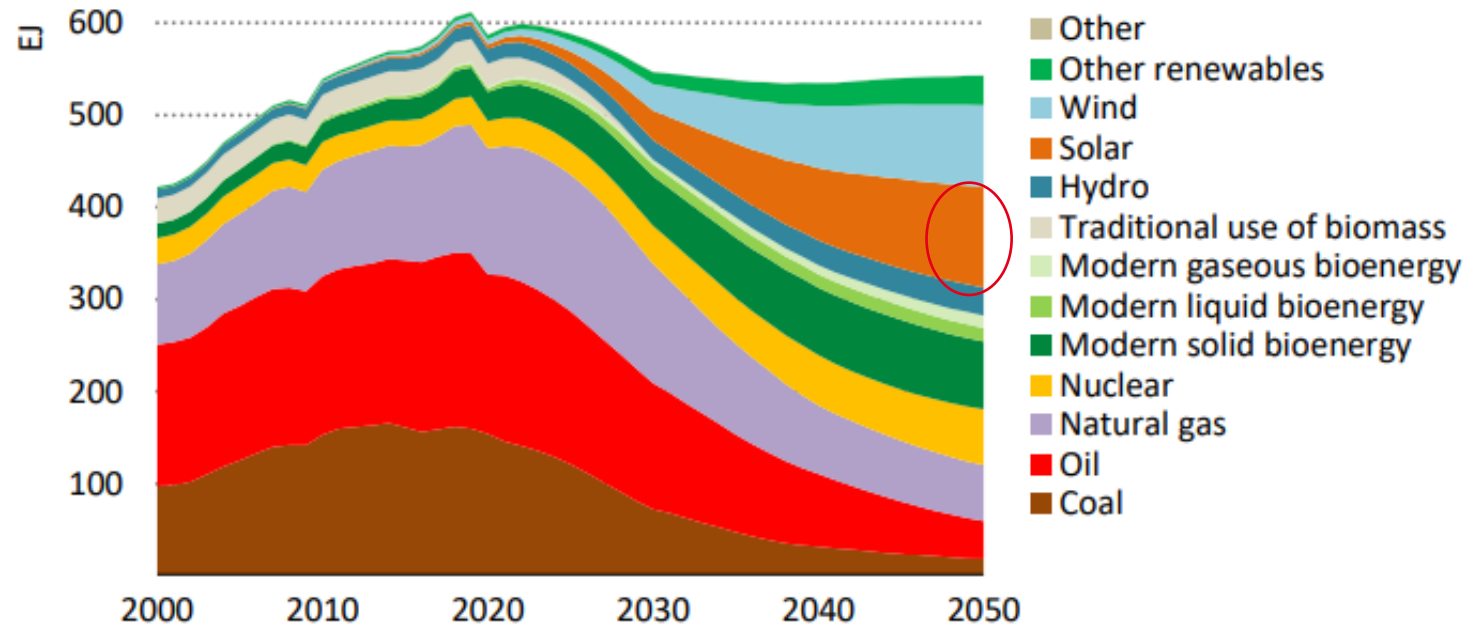
PILOT LINE – 2400 cells/hour

Which place for PV in the world in 2050?



Roadmap for energy supply...

Total energy supply in the NZE



... and corresponding solar PV installed capacity

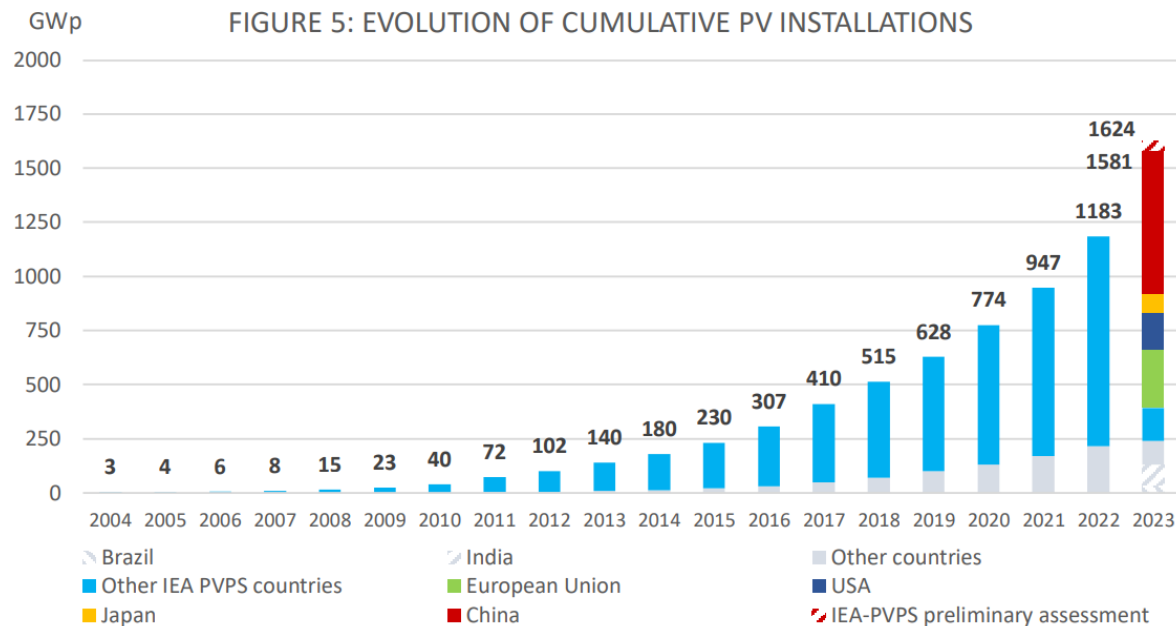
Low scenario from IEA:
14.4 TW of installed capacity

High scenario from ITRPV:
63.4 TW of installed capacity

➔ Net zero CO₂ roadmap from IEA, push to **massive electrification of energy source** from **20% to 50%** of the final energy use

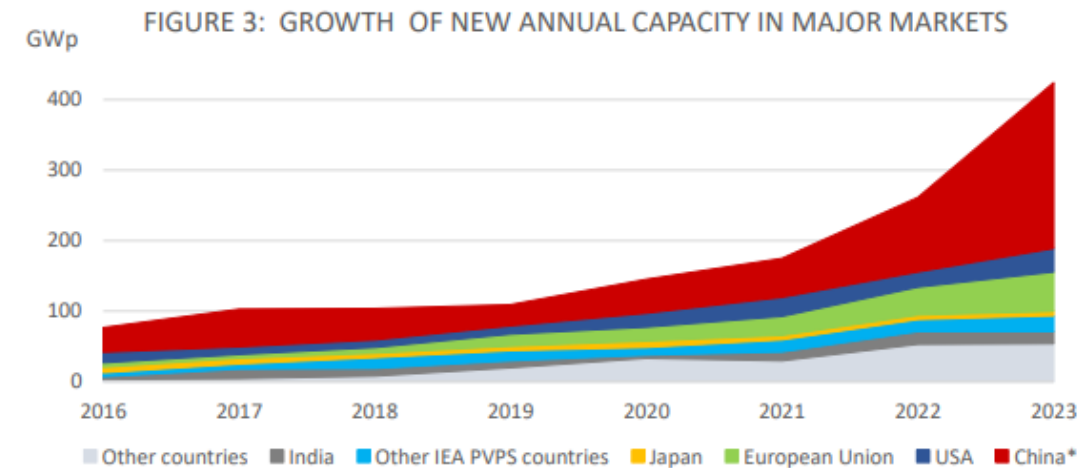
What was the situation in 2023?

Current total installed capacity




- **1.6 TW** of installed capacity mainly in china

Annual capacity growth

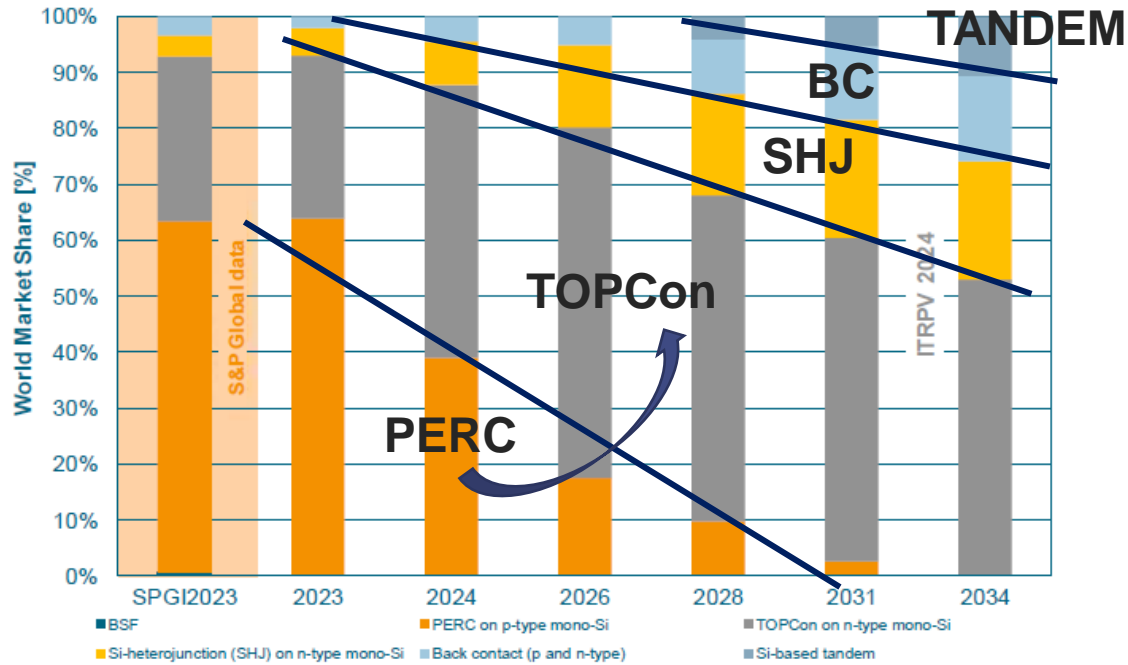


- Important dynamic during the last year
- Additional growth of 210 GW in 2023
- UE ~15% of the market

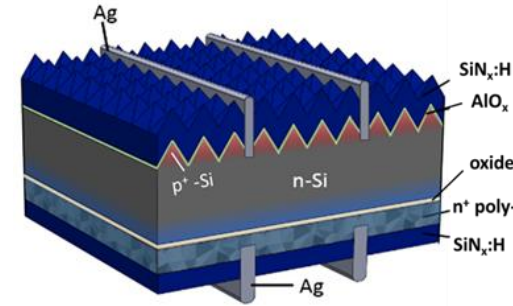

 Targeted production capacity:
Low scenario: +600 GW/year
High scenario: +2300 GW/year

PV solar cell technologies

Technology market share

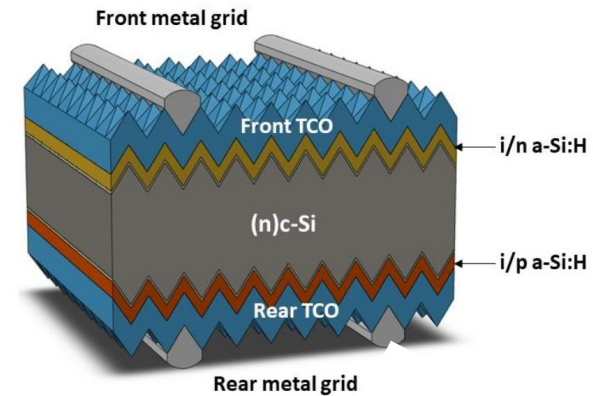


- Silicon based technologies dominate the market
- PERC manufacturers switch to TOPCON technology
- Market share of SHJ should increase from 5% to 20%
- Tandem foreseen as next step



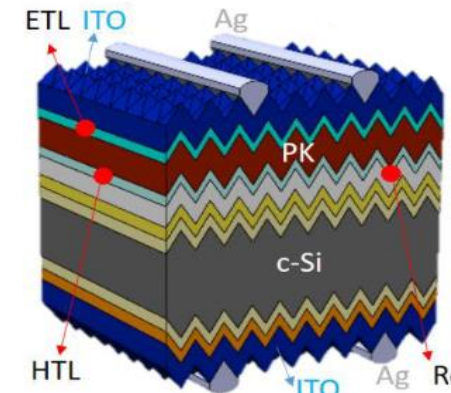
TOPCON

>26%, high TRL



Heterojunction (SHJ)

>26%, high TRL



Tandem (2T,3T,...)

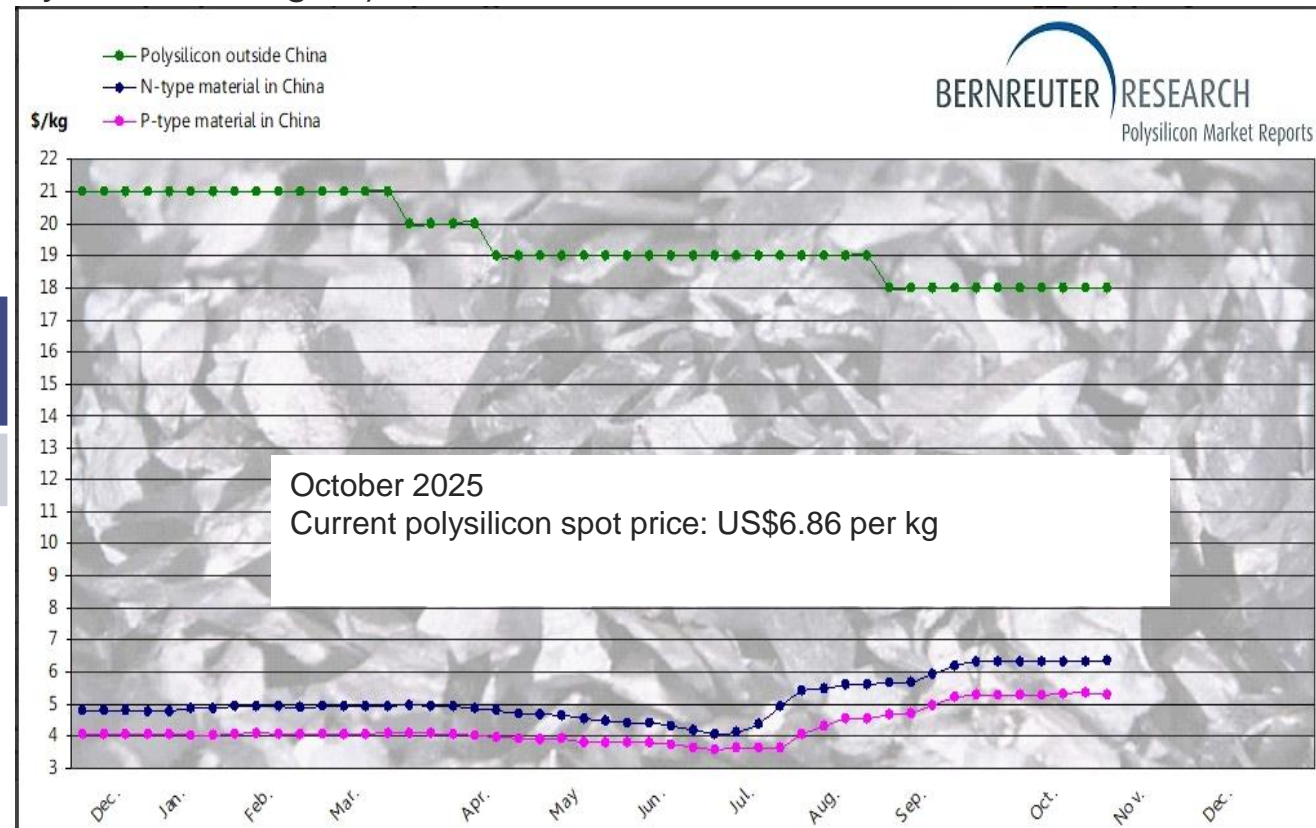
>35% low TRL

Silicon specification used today in PV industry

- **Solar Grade Silicon (SoG-Si): used in most commercial PV modules**
 - Purity: mostly 9N to 12N polysilicon from Siemens or FBR processes
 - Forms: Chunks, rods, granules, chips
- **Upgraded Metallurgical Grade Silicon (UMG-Si): minimum quality requirement for a solar cell**
 - Purity: 6 to 7N, metallurgically refined silicon,
 - Forms: chips, blocks (generally broken from directionally solidified ingots)

Silicon purity scale		
6N	99,9999%	1ppm total impurities
9N	99,9999999%	1ppb total impurities
10N	99,99999999%	0,1ppb total impurities
11N	99,999999999%	0,01ppb total impurities

	Resistivity (Ohm.cm)	Carrier lifetime over resistivity (τ/ρ) $\mu\text{s}/\text{Ohm.cm}$	Interstitial oxygen (O_i) ppma	Substitutional carbon (C_s) ppma
SHJ	0,4 to 1,6	>> 3000	≤ 11	≤ 1



Sustainable silicon production



Evaluation at the ingot level of the photovoltaic properties of the recycled silicon produced

- To assess the suitability of recycled silicon for solar cells production
- To define the best way to produce ingot

Sustainable silicon production

“Recycled silicon” production

Medium scale slag

Silicon purity level reached: 98,91%

Large scale slag @ ELKEM

Silicon purity level reached: 97,79%

Vacuum refining step

Additional purification steps:

Additional directional solidification



Recycled silicon ingot after directional solidification

Additional Cz growth process



Recycled silicon ingot from crystal pulling

[B]= 11 ppmw
[P]=2,6 ppmw
[Al]=0,4 ppmw

Silicon purity in the range of 2N (99%) to 4N (99,99%)

Silicon purity in the range of 5N (99,999%)

[B]= 1,9 ppmw
[P]=2,5 ppmw
[Al]=0,3 ppmw

Sustainable silicon production

- Production of recycled silicon was carried out by NTNU:

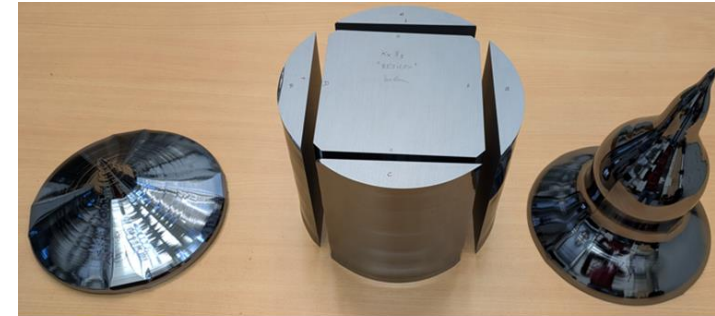


Recycled silicon ingot after directional solidification

- 1st Cz ingot with Boron and Phosphorus concentration added as if we had used 1,8% of the recycled silicon from directional solidification



Resilex Cz ingot n°1



Resilex: M2 brick squaring

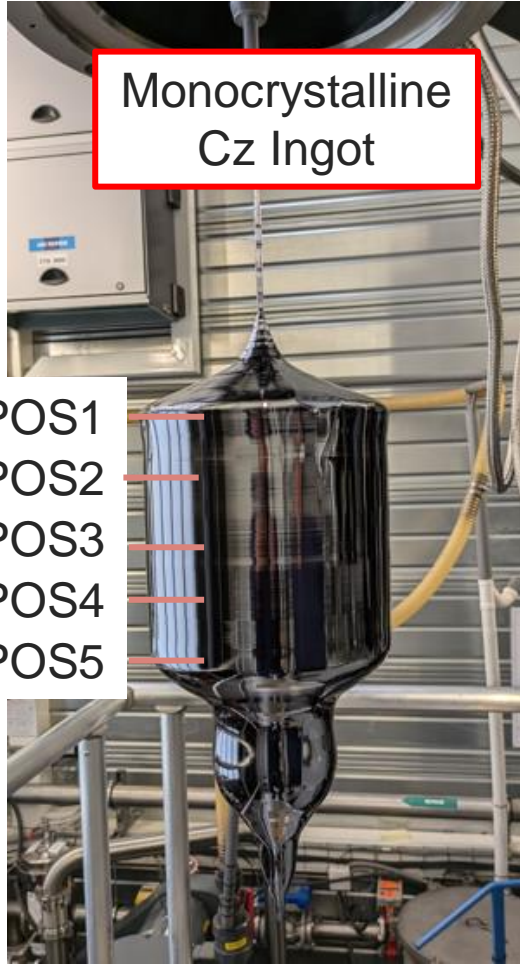


Resilex: Brick wafering

700 mono Cz wafers available

- Assessment of the wafers of the 1st Cz ingot with specific B and P concentration**
 - Measurement of the wafer electrical quality from 5 positions in the ingot
 - Measurement of the material behavior under light

Assessment of the wafers from the 1st Cz ingot



Monocrystalline Cz Ingot

POS1
POS2
POS3
POS4
POS5

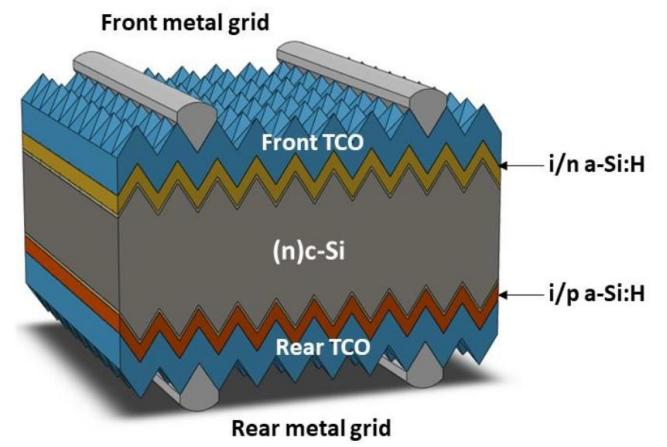
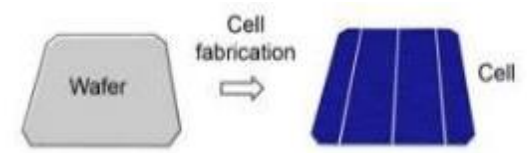
Resilex Cz ingot n°1

Wafer from RESILEX Cz Ingot n°1 (POS 1 to POS 5)

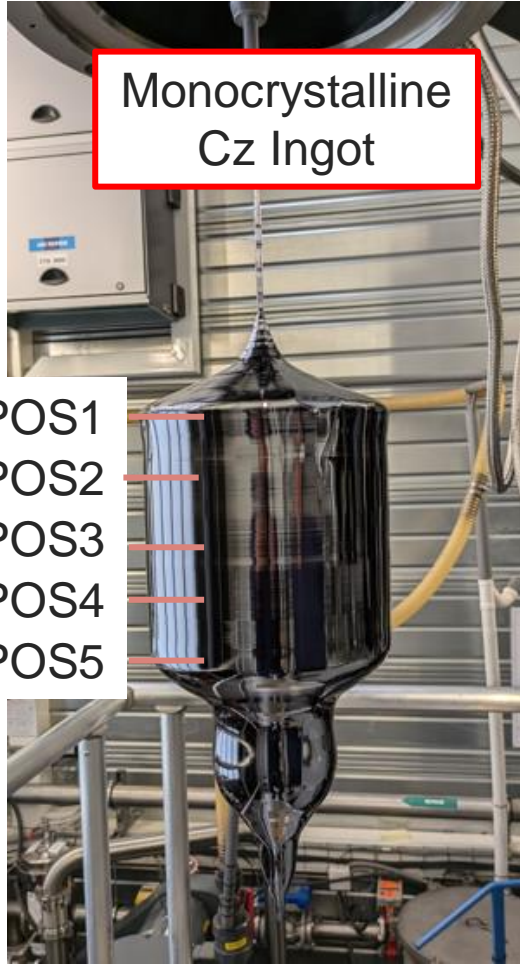
Cleaning Etching to 130 μm

Reference (130 μm Cz n-type wafer from Longi)

- 1) WET chemicals
- 2) PECVD a-Si
- 3) PVD TCO 100 nm Front & Rear
- 4) Screen-printing Ag paste
- 5) Curing



Assessment of the wafers from the 1st Cz ingot



Monocrystalline Cz Ingot

POS1
POS2
POS3
POS4
POS5

Resilex Cz ingot n°1

1) Electrical quality measurement:

→ carrier lifetime @ $1.10^{15} \text{ cm}^{-3}$ injection level

→ Implied V_{oc} (iV_{oc}) @ 1 suns

Wafer from RESILEX Cz Ingot n°1 (POS 1 to POS 5)

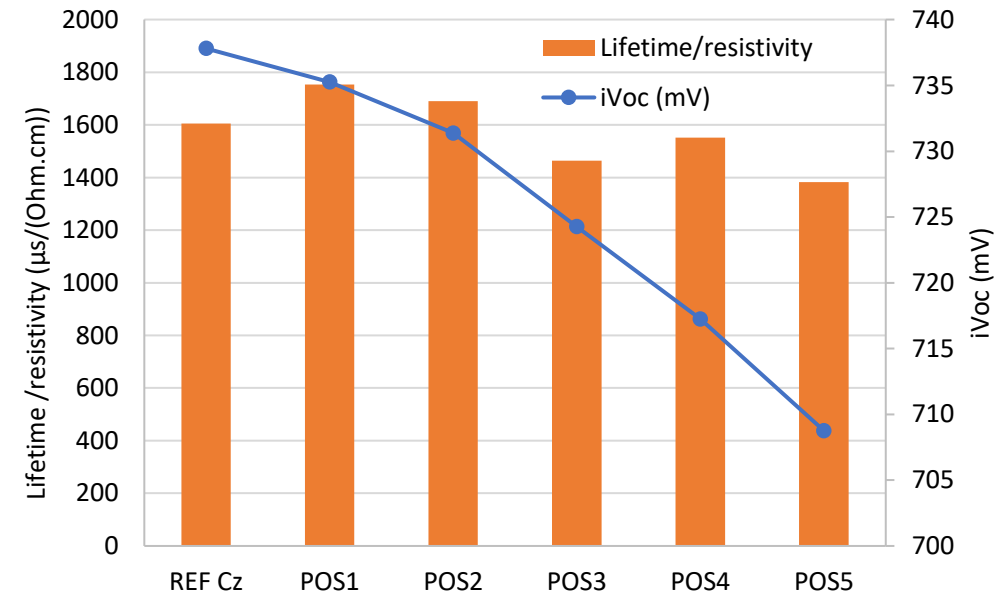
Reference (130 μm Cz n-type wafer from Longi)

Cleaning Etching to 130 μm

1) WET chemicals

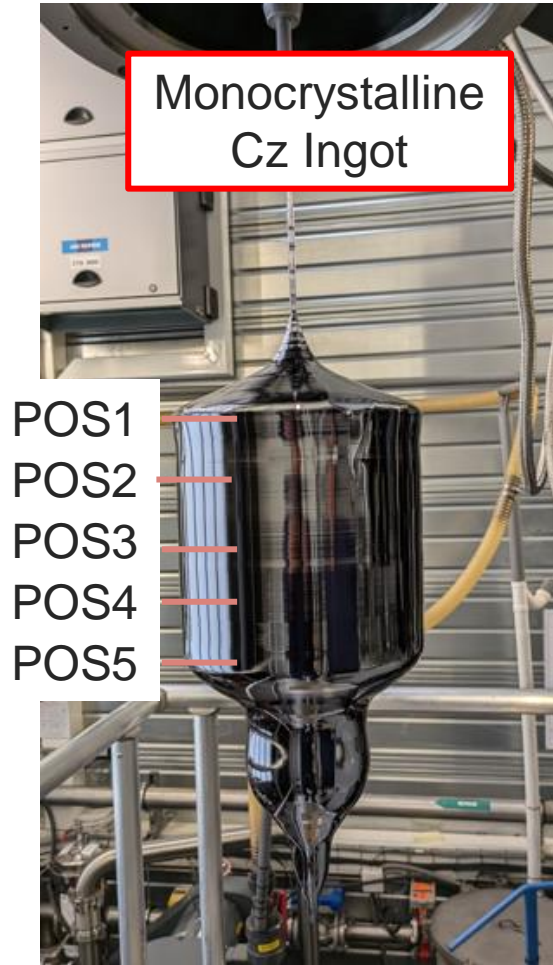
2) PECVD a-Si

Averages values on 5-8 wafers

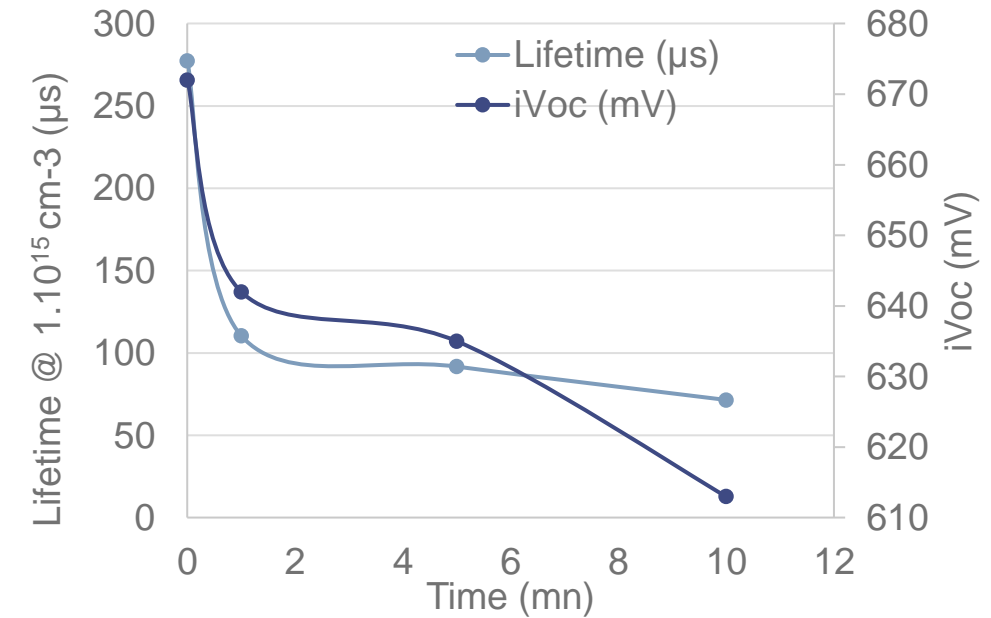
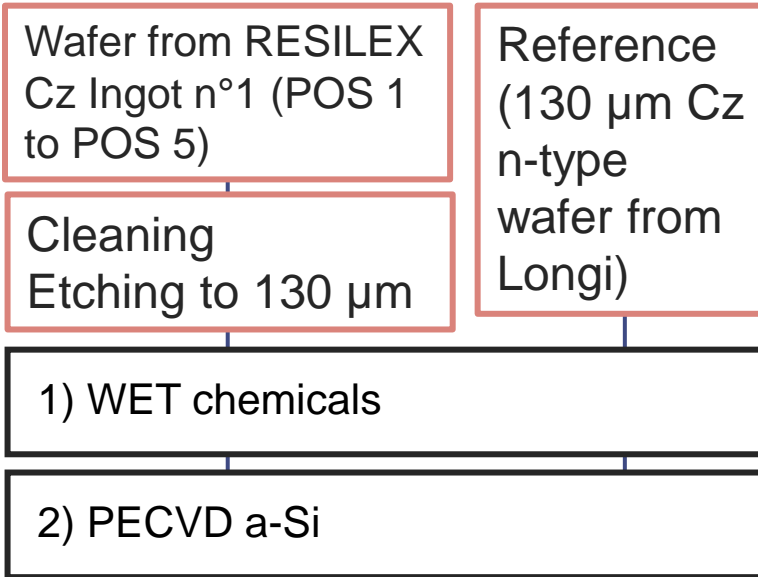


Assessment of the wafers from the 1st Cz ingot

2) Lifetime and iV_{oc} variations under light exposure: 60°C & 0.5 suns



Resilex Cz ingot n°1



- Evolution under light could be explained by the Boron concentration in the wafers
→ Experiment to be continued with regeneration tests to verify that it is B-O defects

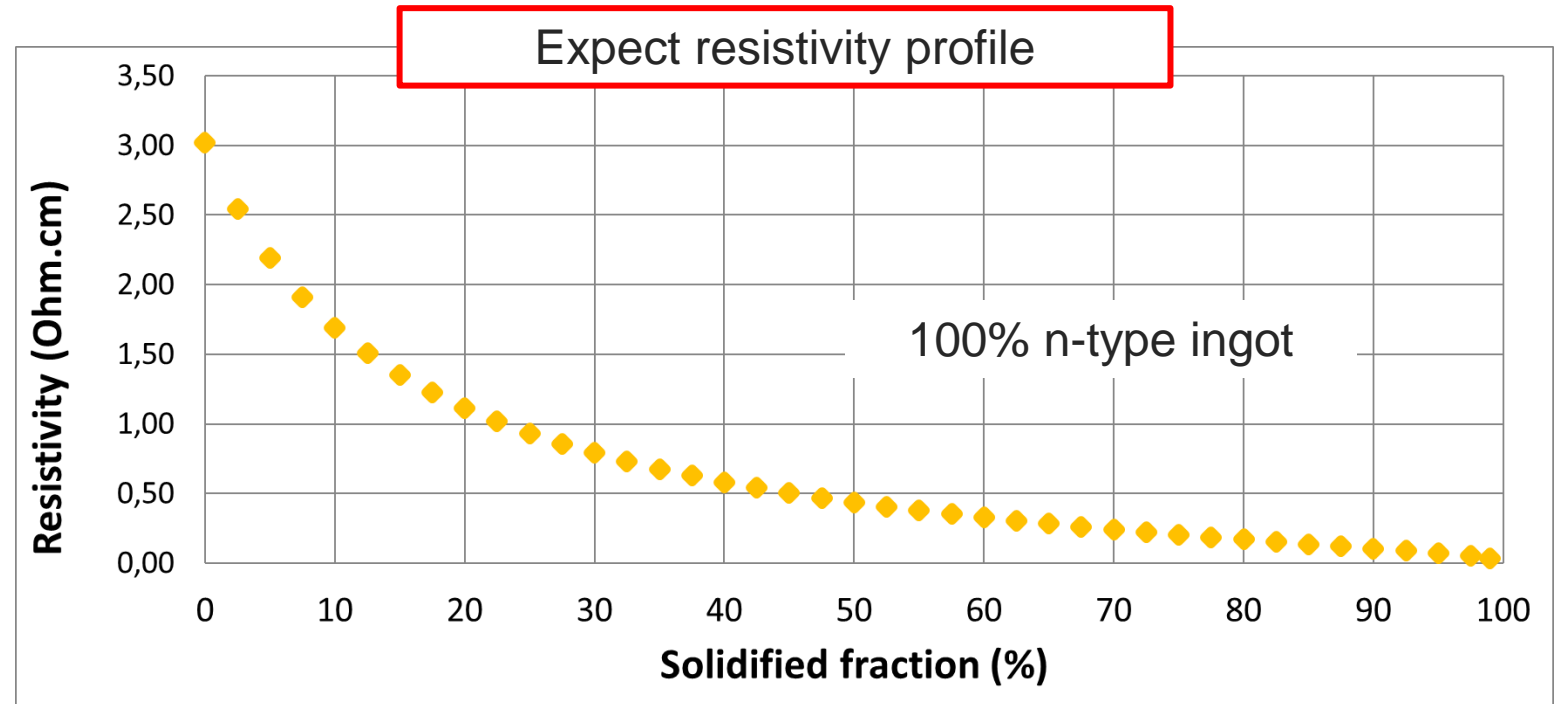
Sustainable silicon production

- Production of recycled silicon was carried out by NTNU:



Recycled silicon ingot after Cz growth process

- 2 Cz ingots using respectively ~11% and ~12% recycled silicon feedstocks from Cz growth process
- Additional dopant [P]: 1,3 ppmw ~6,5 time higher than for a Cz ingot without recycled silicon



Sustainable silicon production

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12% recycled silicon feedstock



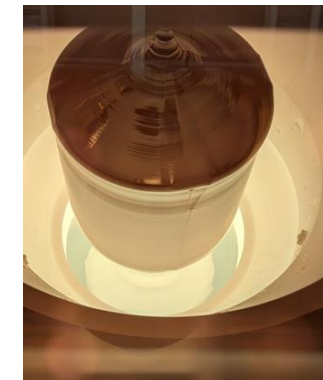
Recycled silicon: 3,5kg



Body step

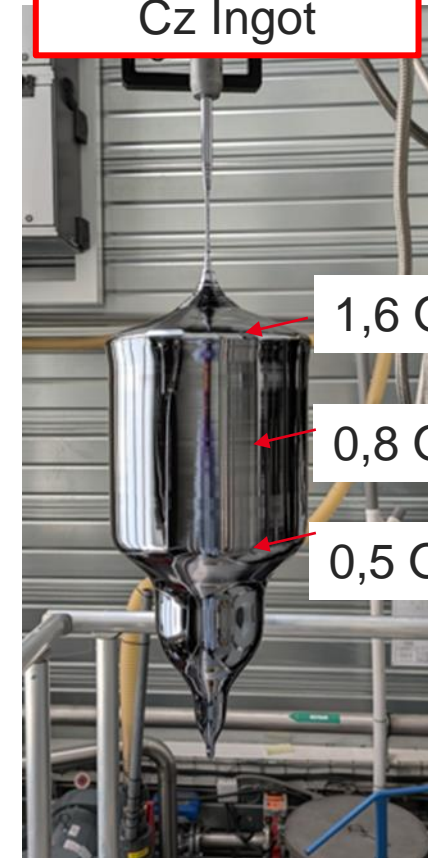


Loaded crucible: 30kg



Final step: Tail

Monocrystalline Cz Ingots



1,6 Ohm.cm

0,8 Ohm.cm

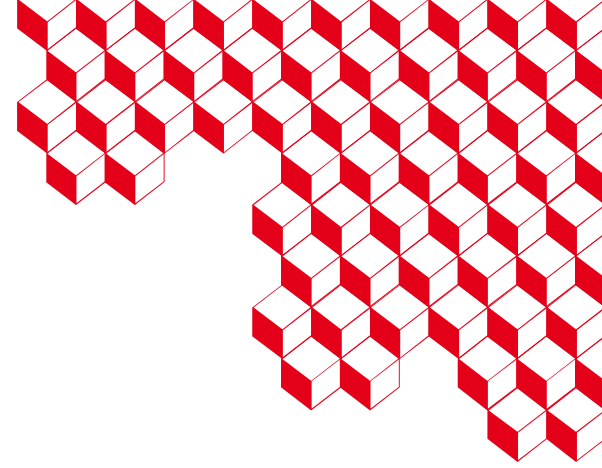
0,5 Ohm.cm

Resilex Cz ingot n°3

Summary and next steps



- NTNU sent to CEA two “recycled silicon” ingots
- CEA performed impurities analyses on the two “recycled silicon” feedstocks to assess the suitability of the waste material for the production of solar cells
- CEA pulled one 30kg Cz ingots using the results of impurity analysis (GDMS) of silicon feedstocks from directional solidification process
- CEA pulled one 30kg Cz ingots using respectively 12% recycled silicon feedstocks from Cz growth process .
- These ingots meet the resistivity and type objectives compatible with the manufacture of heterojunction solar cells on the CEA pilot line.



Thank you!



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